

Substitute for form 1449B/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**






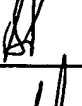
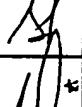
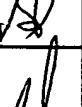

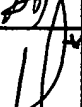
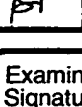
(use as many sheets as necessary)

Sheet 1 of 2

Complete if Known

Application Number	09/665,884 /
Filing Date	September 20, 2000
First Named Inventor	Christopher H. Dick
Group Art Unit	2631
Examiner Name	Not Yet Known
Attorney Docket Number	X-501-1P US

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS

Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		Hwang, Kai, "Computer Arithmetic Principles, Architecture, and Design"; Copyright 1979 by John Wiley & Sons, Inc.; pages 152-155.	
		Marvin E. Frerking, "Digital Signal Processing in Communication Systems", published by Van Nostrand Reinhold, New York, 1994, pp. 124-128 and 171-174.	
		John R. Deller, Jr. et al., "Discrete Time Processing of Speech Signals", published by Macmillan Publishing Company, Maxwell Macmillan International, New York, 1993, pp. 273-280.	
		"The Programmable Logic Data Book", 1999, available from Xilinx, Inc., 2100 Logic Drive, San Jose, California 95124, pp. 6-5 to 6-41.	
		L. Mintzer, "FIR Filters with Field-Programmable Gate Arrays", Journal of VLSI Signal Processing, No. 6, 1993, pp. 119-127.	
		S. Mohanakrishnan et al., "Automatic Implementation of FIR Filters on Field Programmable Gate Arrays", IEEE Signal Processing Letters, Vol. 2, No. 3, March 1995, pp. 51-53.	
		D. Patterson et al., "Computer Architecture: A Quantitative Approach", Morgan Kaufmann Publishers, Inc., California, 1990, pp. A8-A10.	
	*	C.H. Dick and F.J. Harris; "FPGA Signal Processing Using Sigma-Delta Modulation"; IEEE Signal Processing Magazine, January 2000	
		Atmel AT40K/05/10/20/40 Data Sheet, 1999	
	*	A. Peled and B. Liu, "A New Hardware Realization of Digital Filters" IEEE Trans. on Acoust., Speech, Signal Processing, Vol. 22, pp. 456-462, December 1974.	
	*	S.A. White, "Applications of Distributed Arithmetic to Digital Signal Processing" IEEE ASSP Magazine, Vol. 6(3), pp. 4-19, July 1989	

Examiner Signature

Date Considered

02/28/04

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Unique citation designation number. ² Applicant is to place a check mark here if English language Translation is attached.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Examiner Signature		Date Considered	02/20/04
-----------------------	---	--------------------	----------

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Unique citation designation number. ² Applicant is to place a check mark here if English language Translation is attached.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Office, Washington, DC 20231. **DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO:** Assistant Commissioner for Patents, Washington, DC 20231.